



**PATENT APPLICATION**

**FILING UNDER 37 CFR §1.312  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER ART UNIT 2826**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

ATTN: OFFICE OF PUBLICATIONS

Isamu KUNO et al.

Notice of Allowance Mailed 03/23/2005 -  
Batch/Confirmation No. 6743

Application No.: 10/773,213

Group Art Unit: 2826

Filed: February 9, 2004

Examiner: F. ABRAHAM

Docket No.: 118423

OK  
to  
ENR  
For: SEMICONDUCTOR INTEGRATED CIRCUIT INCLUDING ANTI-FUSE DEVICE  
AND METHOD FOR MANUFACTURING THE SAME

**AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312**

NO  
S/19/05  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the March 23, 2005 Office Action, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**